

10/567991

Attorney's Docket No.: 19415-007US1 / PCT-04R-166/US

IAP20 Rec'd PCT/PCT 09 FEB 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hidetoshi Nishikawa Art Unit : Unknown
Serial No. : Examiner : Unknown
Filed : February 9, 2006
Title : SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-145

INFORMATION DISCLOSURE STATEMENT

Applicant requests consideration of the references listed and enclosed on the attached PTO-1449 form. An International Search Report (ISR) is also enclosed. The relevance of the references is indicated on the ISR.

This statement is being filed with the application. Please apply any charges or credits to
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Respectfully submitted,

Date: 2/9/06

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Substitute Form PTO-1449 (Modified)		U.S. Department of Commerce Patent and Trademark Office		Attorney's Docket No. 19415-007US1	Application No. 10/1567091
Information Disclosure Statement by Applicant (Use several sheets if necessary)				Applicant Hidetoshi Nishikawa	
				Filing Date February 9, 2006	Group Art Unit
(37 CFR §1.98(b))					

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
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Foreign Patent Documents or Published Foreign Patent Applications							
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation
							Yes No
	AL	51-21466	02/20/1976	Japan			X
	AM	61-214622	09/24/1986	Japan			X
	AN	6-141333	05/20/1994	Japan			X
	AO	2004-140752	05/13/2004	Japan			X
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Other Documents (include Author, Title, Date, and Place of Publication)							
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EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	